

1 156. The method of claim 151 further including providing the first  
2 amount of data corresponding to the first block size information to the  
3 memory device.

1 157. The method of claim 156 wherein the first amount of data is  
2 provided to the memory device after a delay time transpires.

1 161. A method of operation [of] in a synchronous memory device,  
2 wherein the memory device includes a plurality of memory cells, the  
3 method of operation of <sup>in</sup> the memory device comprises:

4 receiving first block size information from a [bus] controller,  
5 wherein the first block size information defines a first amount of data  
6 to be input by the memory device in response to a write request;

7 receiving a first write request from the [bus] controller  
8 synchronously with respect to an external clock signal; and

9 inputting the first amount of data corresponding to the first  
10 block size information in response to the first write request.

1 162. The method of claim 161 wherein the first amount of data  
2 corresponding to the first block size information is sampled  
3 synchronously with respect to the external clock signal.

1 <sup>13</sup> 163. The method of claim <sup>11</sup> 161 further including:  
2 receiving second block size information, wherein the second block  
3 size information defines a second amount of data to be input in  
4 response to a [second] write request;  
5 receiving a second write request from the ~~bus~~ controller; and  
6 inputting the second amount of data corresponding to the second  
7 block size information, in response to the second write request.

*Sub. E1* 1 <sup>26</sup> 174. The method of claim <sup>23</sup> 171 wherein the [first] block size  
2 information is a binary representation of the first amount of data to  
3 be input in response to the first write request.

*B4* 1 <sup>27</sup> 175. The method of claim <sup>23</sup> 171 wherein the [first] block size  
2 information is provided by a controller.

☒ Kindly **ADD** the following claims:

*Sub. E1* 1 <sup>21</sup> 176. The method of claim <sup>11</sup> 161 wherein the first amount of data is  
2 input, in response to receipt of the first write request, after a delay  
3 time transpires.

*B5* 1 <sup>22</sup> 177. The method of claim <sup>21</sup> 176 wherein the delay time is  
2 representative of a number of clock cycles of the external clock signal  
3 that transpire before the first amount of data is input.